

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S61	76	S60 and @ad<="20030826"	US-PGPUB; USPAT	OR	ON	2007/03/02 16:42
S60	93	(scan near2 only) same (latch latches LSSD (Level near2 Sensitive near2 Scan near2 Design)) same (fuse pattern shift f\$source scanin fusein scan\$in fuse\$in)	US-PGPUB; USPAT	OR	ON	2007/03/02 15:24
S59	34	Anand-D\$.in.	US-PGPUB; USPAT	OR	ON	2007/03/02 15:21
S58	1984	Anand.in.	US-PGPUB; USPAT	OR	ON	2007/03/02 15:07
S53	133	S52 and @ad<="20030826"	US-PGPUB; USPAT	OR	ON	2007/03/02 15:06
S57	3	((fuse efuse) with skip with multipl\$4) and @ad<="20030826"	US-PGPUB; USPAT	OR	ON	2007/03/01 14:44
S56	1	"20040066695".pn.	US-PGPUB; USPAT	OR	ON	2007/03/01 14:42
S55	45	S54 and @ad<="20030826"	US-PGPUB; USPAT	OR	ON	2007/03/01 14:41
S54	49	((("NAND" nand ("not" adj "and") (N\$and)) with (gate logic)) same scan same (shift\$4 function\$4) same (latch latches LSSD (Level near2 Sensitive near2 Scan near2 Design)) same ((single one sole) near5 (input port latch lssd (Level near2 Sensitive near2 Scan near2 Design))))	US-PGPUB; USPAT	OR	ON	2007/03/01 14:22
S52	177	((("NAND" nand ("not" adj "and") (N\$and)) with (gate logic)) same scan same (shift\$4 function\$4) same (latch latches LSSD (Level near2 Sensitive near2 Scan near2 Design))	US-PGPUB; USPAT	OR	ON	2007/03/01 14:21
S51	605	((("NAND" nand ("not" adj "and") (N\$and)) with (gate logic)) same scan same (shift\$4 function\$4)	US-PGPUB; USPAT	OR	ON	2007/03/01 14:14
S47	183	S46 and @ad<="20030826"	US-PGPUB; USPAT	OR	ON	2007/03/01 14:14
S50	59	S48 and S49	US-PGPUB; USPAT	OR	ON	2007/03/01 14:09
S49	118	(select\$5 filter\$4 separat\$5 break\$4 choos\$4 chos\$4 pick\$4 determin\$4) same (latch latches LSSD (Level near2 Sensitive near2 Scan near2 Design)) same ((single one sole) near5 (input port latch)) same scan same shift same clock	US-PGPUB; USPAT	OR	ON	2007/03/01 11:49

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S46	217	(select\$5 filter\$4 separat\$5 break\$4 choos\$4 chos\$4 pick\$4 determin\$4) same (latch latches LSSD (Level near2 Sensitive near2 Scan near2 Design)) same ((single one sole) near5 (input port latch)) same scan same shift	US-PGPUB; USPAT	OR	ON	2007/03/01 11:49
S48	119	S47 not S45	US-PGPUB; USPAT	OR	ON	2007/03/01 11:36
S45	64	S43 and @ad<="20030826"	US-PGPUB; USPAT	OR	ON	2007/03/01 11:36
S41	179	((input) with (circuit unit device module apparatus)) same (latch latches LSSD (Level near2 Sensitive near2 Scan near2 Design)) same ((single one sole) near5 (input port latch)) same scan same shift	US-PGPUB; USPAT	OR	ON	2007/03/01 11:31
S44	156	S42 and @ad<="20030826"	US-PGPUB; USPAT	OR	ON	2007/03/01 10:53
S43	70	S41 same (select\$5 filter\$4 separat\$5 break\$4)	US-PGPUB; USPAT	OR	ON	2007/03/01 10:50
S42	175	S41 and (select\$5 filter\$4 separat\$5 break\$4)	US-PGPUB; USPAT	OR	ON	2007/03/01 10:48
S38	9	S37 and @ad<="20030930"	US-PGPUB; USPAT	OR	ON	2007/03/01 10:38
S40	1	"7089136".pn.	US-PGPUB; USPAT	OR	ON	2007/03/01 10:22
S39	9	S37 and @ad<="20030826"	US-PGPUB; USPAT	OR	ON	2007/03/01 09:51
S37	11	(efuse effuse Efuse Effuse fuse) same scan same shift same (MUX mux multiplexer multiplexor multipl\$6) same (logic gate)	US-PGPUB; USPAT	OR	ON	2007/03/01 09:44
S36	1492	(efuse effuse Efuse Effuse fuse) and scan and shift and (MUX mux multiplexer multiplexor multipl\$6) and (logic gate)	US-PGPUB; USPAT	OR	ON	2007/03/01 09:44
S35	861	(efuse effuse Efuse Effuse fuse) and scan and shift and (MUX mux multiplexer multiplexor multipl\$6) and (logic gate)	USPAT	OR	ON	2007/03/01 09:43
S9	0	(efuse effuse Efuse Effuse) and scan and shift and (MUX or mux or multiplexer)	USPAT	OR	ON	2007/03/01 09:42